

Altium Logo Top1
Altium



Altium Logo Bot1
Altium



LOGO2
Nanoboard Logo



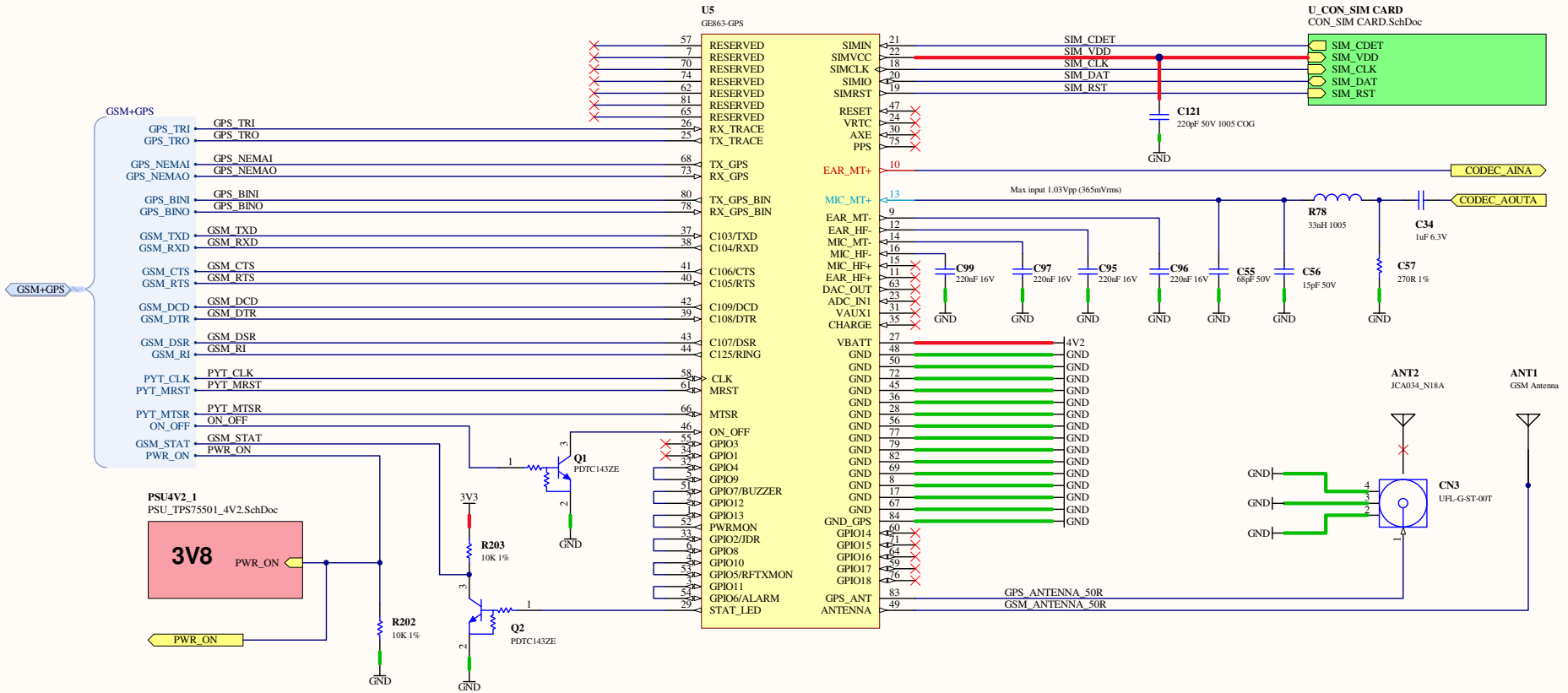
U_1WireID
1WB_DS2406_EEPROM.SchDoc



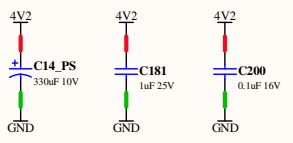
PCB1
NB2HND_BB Blank PCB

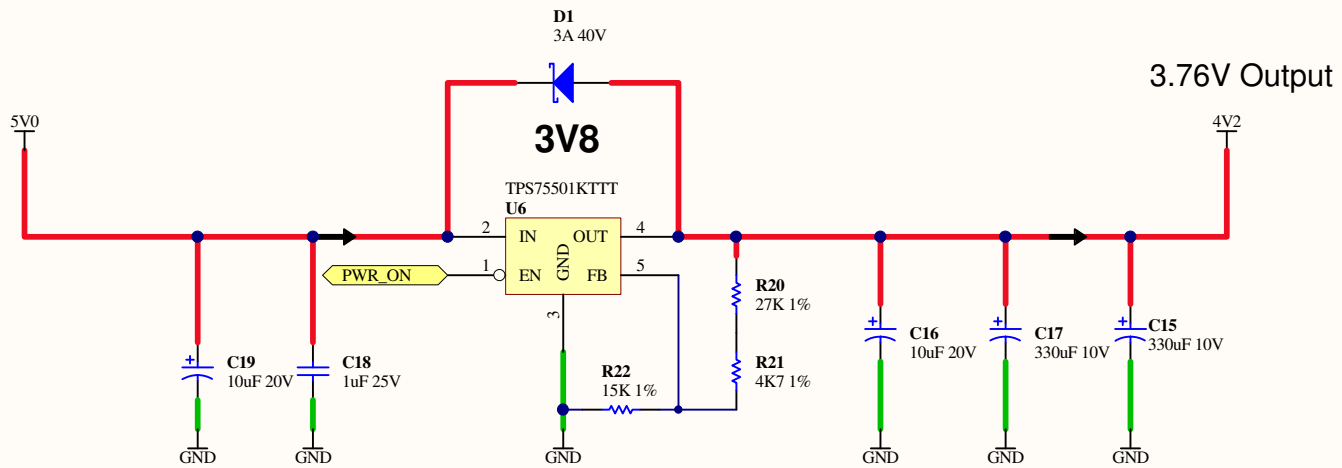


Sheet Title Battery Board Interface			Altium Limited. Level 3, 12a Rodborough Rd Frenchs Forest 2086 NSW AUSTRALIA	
Project Title NB2HND01-BB01				
Size: A3	Assy: D-820-0017	Revision: .02		
Date: 9/23/2011	Time: 12:11:04 AM	Sheet 1 of 11		
File: NB2HND-BB01.SchDoc				



GSM+GPS POWER





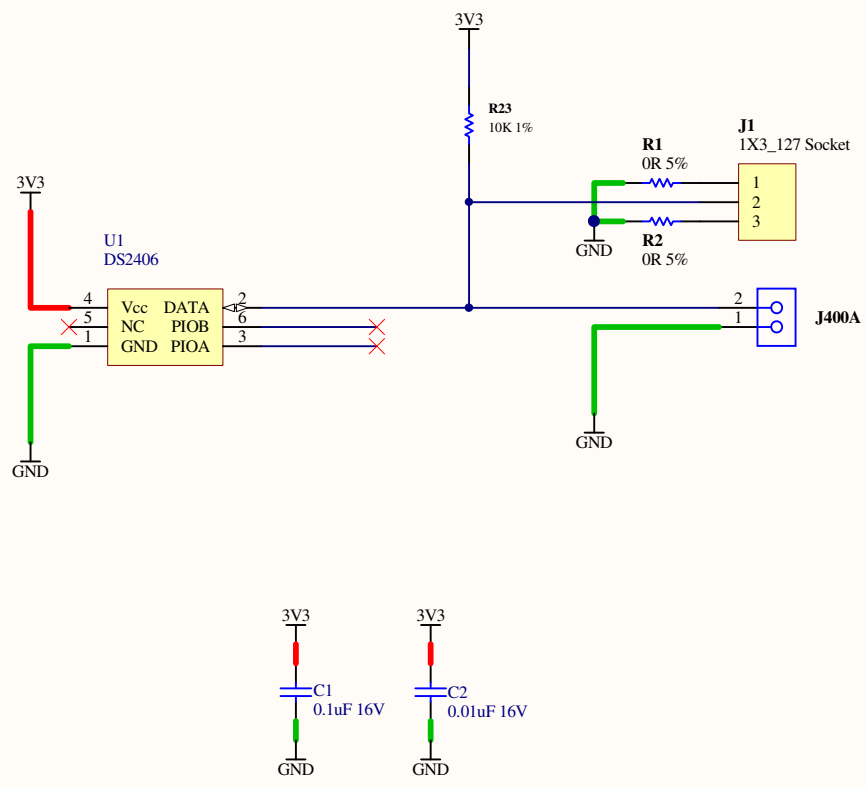
$V_o = 1.224V * (1 + ((R20 + R21) / R23)) = 3.762V$

1uF ceramic input cap required for LDO stability.
 >47uF solid tantalum with ESR > 200mOhms required on output for stability.

Primary 3.8V Supply

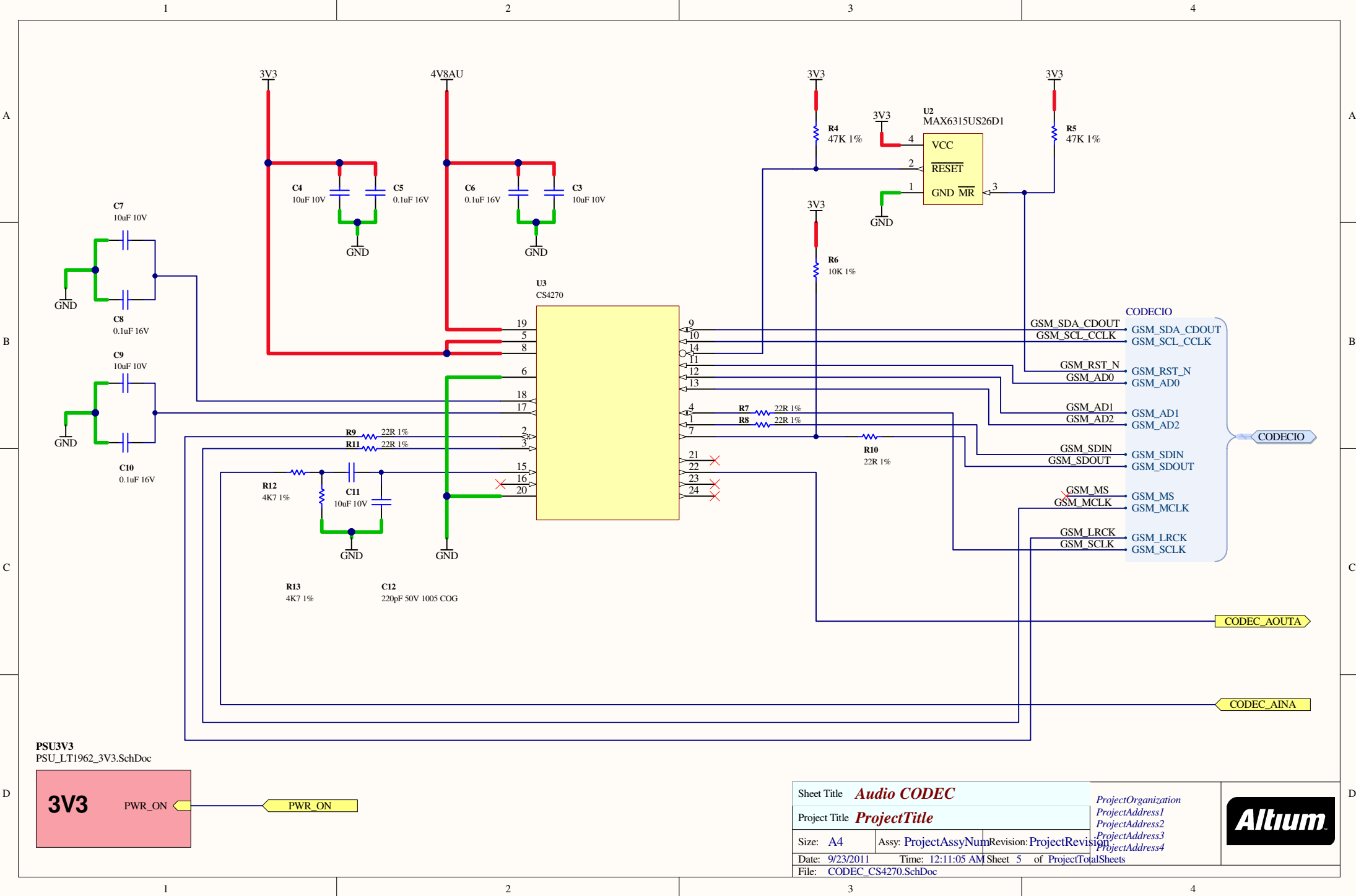
Sheet Title PSU_TPS75501_4V2		Project Organization	
Project Title ProjectTitle		ProjectAddress1	
Size: A4	Number: 3	Revision: ProjectRevision	ProjectAddress2
Date: 9/23/2011	Time: 12:11:04 AM	Sheet 3 of ProjectTotalSheets	ProjectAddress3
File: PSU_TPS75501_4V2.SchDoc		ProjectAddress4	



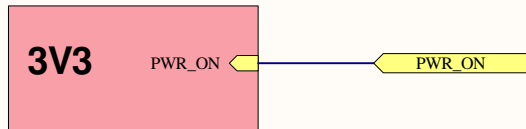


Sheet Title <i>1-Wire Bus ID</i>		Project Organization	
Project Title <i>ProjectTitle</i>		ProjectAddress1	
Size: A4		Assy: ProjectAssyNum	Revision: ProjectRevision
Date: 9/23/2011	Time: 12:11:04 AM Sheet 4 of ProjectTotalSheets		
File: 1WB_DS2406_EPROM.SchDoc		ProjectAddress3	
		ProjectAddress4	



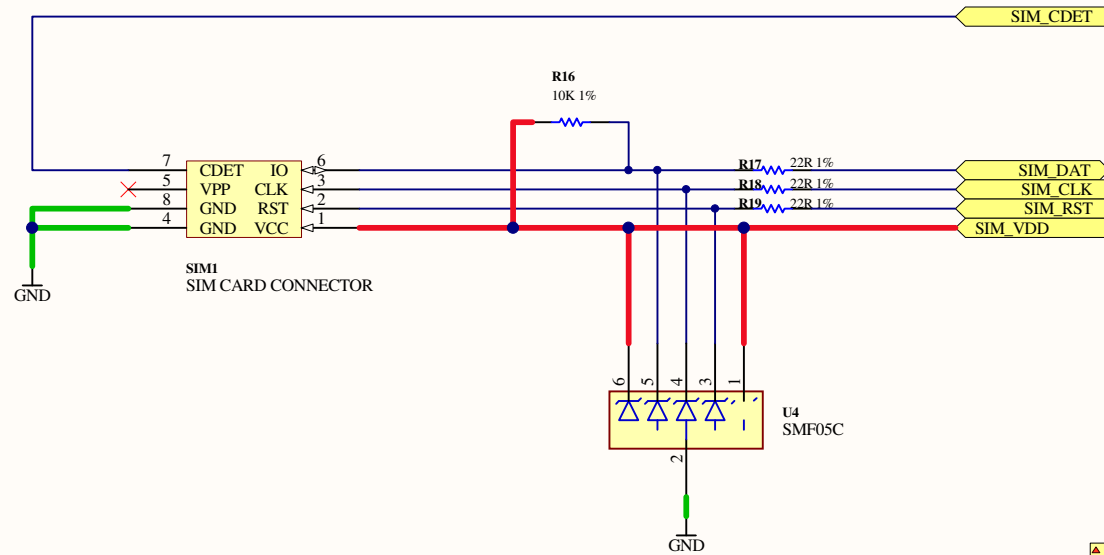


PSU3V3
PSU_LT1962_3V3.SchDoc



Sheet Title Audio CODEC		Project Organization	
Project Title ProjectTitle		ProjectAddress1	
Size: A4		ProjectAddress2	
Date: 9/23/2011		ProjectAddress3	
Time: 12:11:05 AM		ProjectAddress4	
File: CODEC_CS4270.SchDoc		Sheet 5 of ProjectTotalSheets	

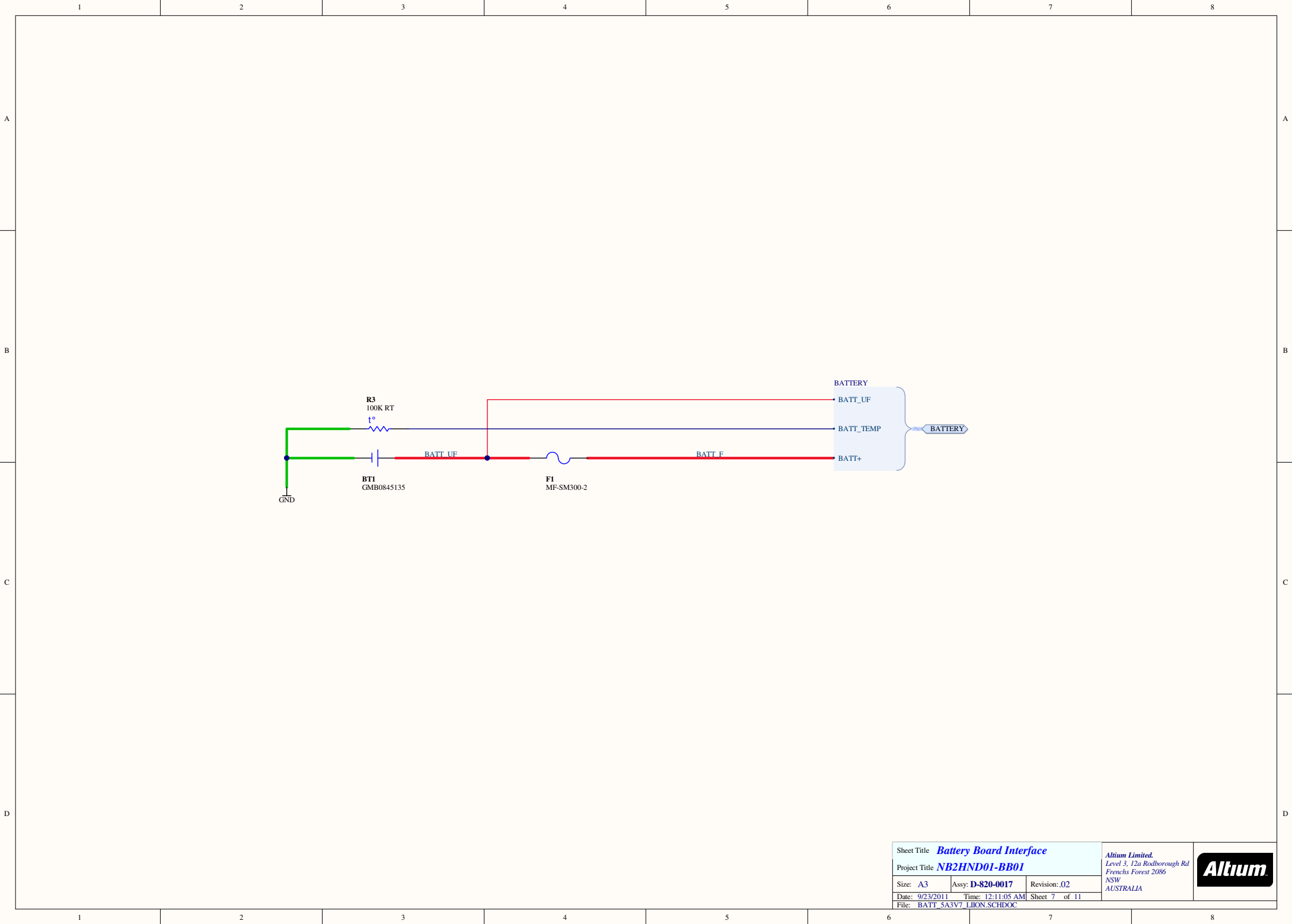





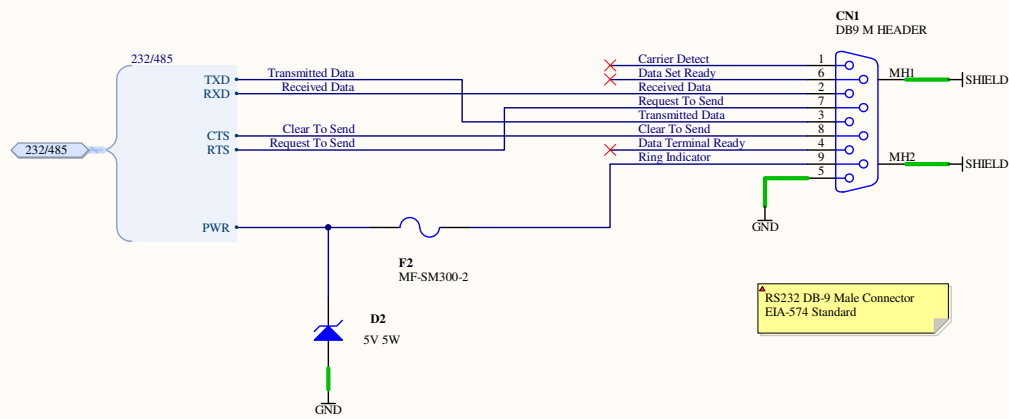
▲ SIM_VDD can identify automatically the SIM card power mode; either 3.0V or 1.8V


Sheet Title CON_SIM_CARD		Project Organization	
Project Title ProjectTitle		ProjectAddress1	
Size: A4	Assy: ProjectAssyNum	Revision: ProjectRevision	ProjectAddress2
Date: 9/23/2011	Time: 12:11:05 AM	Sheet 6 of ProjectTotalSheets	ProjectAddress3
File: CON_SIM_CARD.SchDoc		ProjectAddress4	

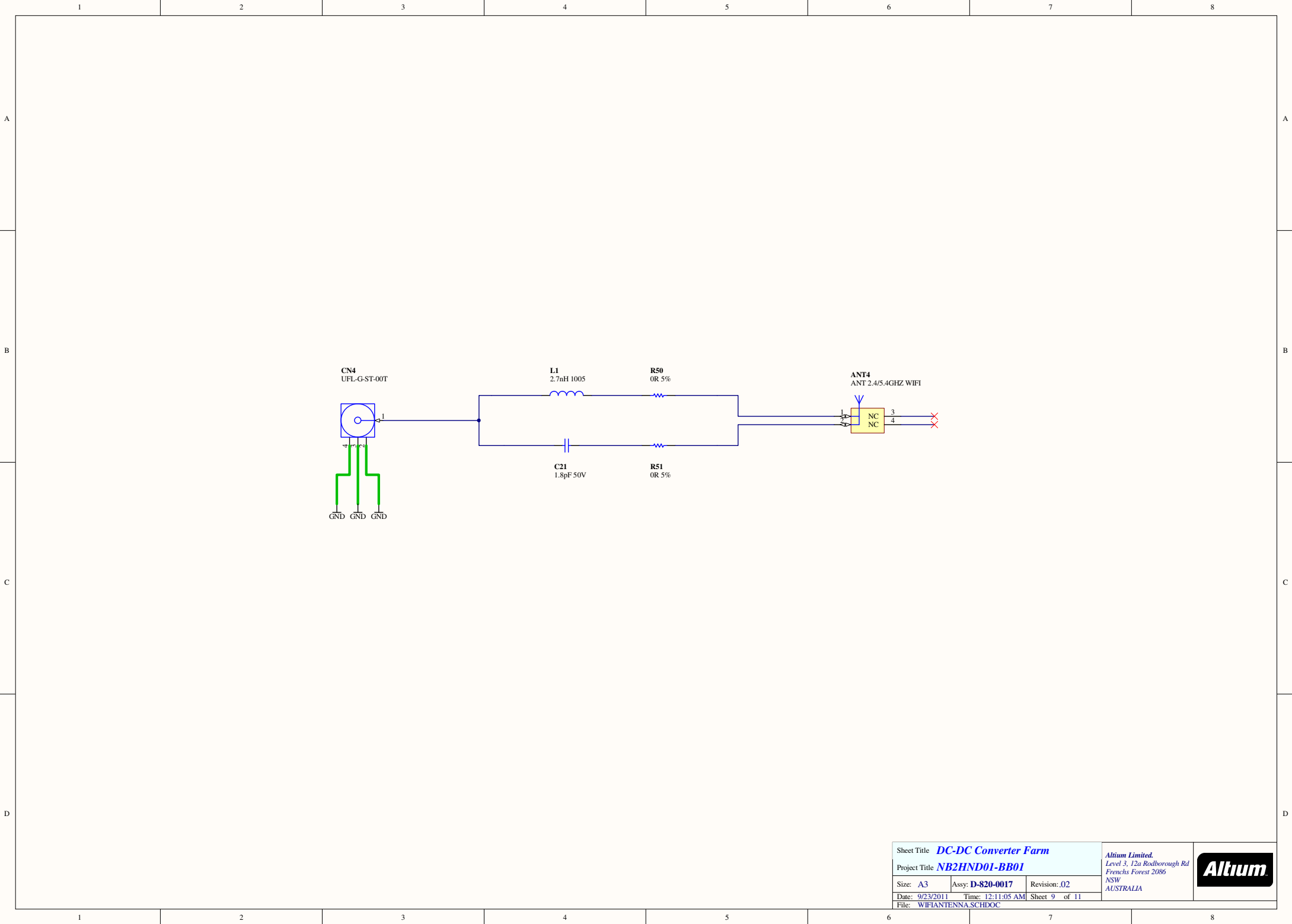





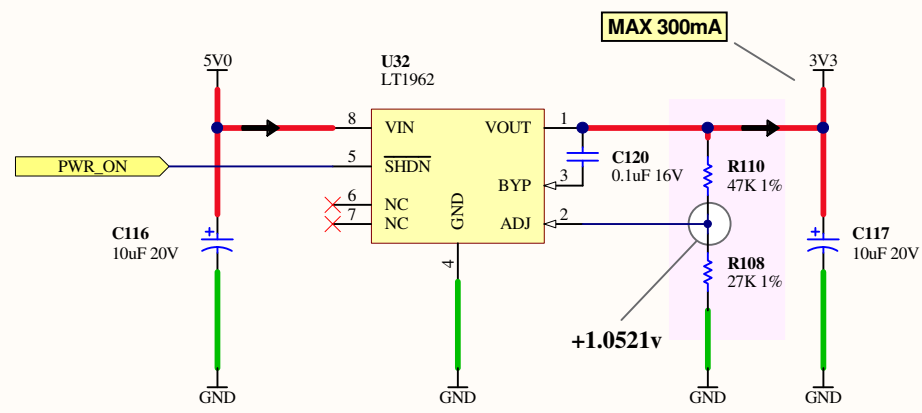
Sheet Title Battery Board Interface			<i>Altium Limited.</i> Level 3, 12a Rodborough Rd Frenchs Forest 2086 NSW AUSTRALIA	
Project Title NB2HND01-BB01				
Date: 9/23/2011	Assy: D-820-0017	Revision: .02		
File: BATT_SA3V7_LIION.SCHDOC		Time: 12:11:05 AM	Sheet 7 of 11	



Sheet Title Battery Pack			<i>Altium Limited.</i> Level 3, 12a Rodborough Rd Frenchs Forest 2086 NSW AUSTRALIA	
Project Title NB2HND01-BB01				
Size: A3	Assy: D-820-0017	Revision: .02		
Date: 9/23/2011	Time: 12:11:05 AM	Sheet 8 of 11		
File: CON_DB9M_VERT.SCHDOC				



Sheet Title DC-DC Converter Farm			Altium Limited. Level 3, 12a Rodborough Rd Frenchs Forest 2086 NSW AUSTRALIA	
Project Title NB2HND01-BB01				
Date: 9/23/2011	Assy: D-820-0017	Revision: .02		
File: WIFIANTENNASCHDOC	Time: 12:11:05 AM	Sheet 9 of 11		



R1 NOT TO EXCEED 250K

$$V_{out} = 1.22 \left(1 + \frac{R2}{RT}\right) - [(I_{Adj})(R2)]$$

$$I_{Adj} = 30nA$$

$$V_{out} = 1.22 \left(1 + \frac{47,000}{27,000}\right) - [(0.000,000,030)(200,000)]$$

$$V_{out} = (1.22 \times 2.74) - 0.0066$$

$$V_{out} = 3.3428 - 0.0066$$

$$V_{out} = 3.33v$$

Sheet Title Audio CODEC		Project Organization	
Project Title ProjectTitle		ProjectAddress1	
Size: A4	Assy: ProjectAssyNum	Revision: ProjectRevision	ProjectAddress2
Date: 9/23/2011	Time: 12:11:05 AM	Sheet 10 of ProjectTotalSheets	ProjectAddress3
File: PSU_LT1962_3V3.SchDoc		ProjectAddress4	



